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SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR
(AUTONOMOUS)**MCA I Year I Semester (R16) Regular Examinations January 2017****COMPUTER ORGANIZATION**

(For Students admitted in 2016 only)

Time: **3 hours**Max. Marks: **60**(Answer all Five Units **5 X 12 = 60** Marks)**UNIT-I**

- Q.1** a. Explain binary multiplication with suitable examples 6M
 b. What is BCD code write the conversion steps of BCD to binary code? 6M

OR

- Q.2** a. Explain JK flip-flop and Trigger flip-flop 6M
 b. Describe full adder and its circuit diagram. 6M

UNIT-II

- Q.3** What is Micro programming explain with suitable examples 12M

OR

- Q.4** Explain address sequencing 12M

UNIT-III

- Q.5** a. Difference between a direct and indirect address instruction. 5M
 b. Instruction formats. 7M

OR

- Q.6** Write a short note on conditional, unconditional and transfer instructions. 12M

UNIT-IV

- Q.7** a. Write short notes on Priority Interrupts? 6M
 b. Explain Peripheral Devices. 6M

OR

- Q.8** Explain in detail about Input Output Processors 12M

UNIT-V

- Q.9** Explain about Interprocessor Communication and Synchronization 12M

OR

- Q.10** Explain RISC pipeline with neat sketch 12M

***** END *****